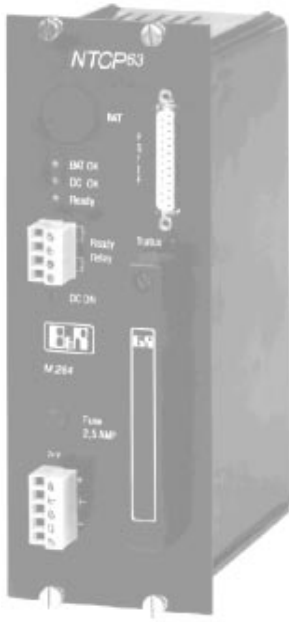


A6

CPUS, NTCP6# - MULTICONTROL CPU TYPE B PLC SYSTEMS MULTICONTROL COMPONENTS



NTCP6#

- 42 KByte Application Program Memory (Max. 42 K Instructions)
- Processing Time¹⁾ Optional (1.7 or 2.5 msec/K Instructions)
- 7168 Registers
800 Flags
- MOTOROLA 6809 Microprocessor
- Status LED
- RS485/RS232/TTY Application Interface
- Date/Time Function (Real-Time Clock)
- Software is Compatible to all Type B CPUs and Parallel Processors

SLOTS

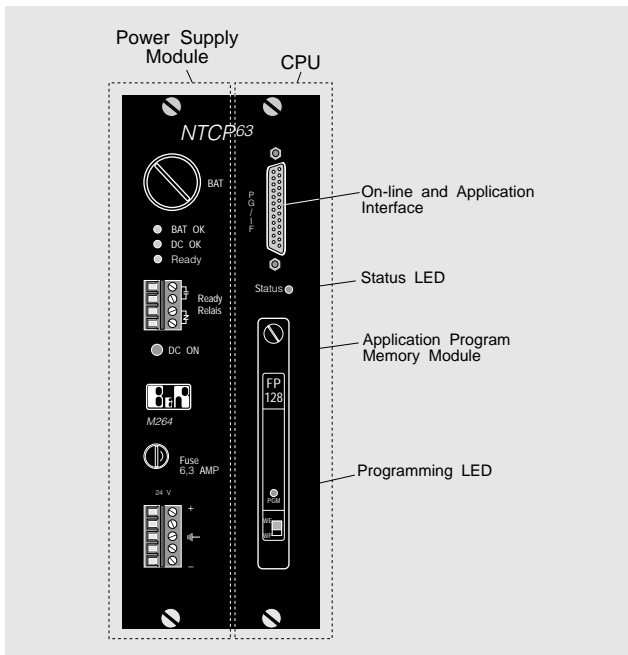
The NTCP6# power supply/CPU module can only be operated in the assigned slot of the M264 rack.

ORDER DATA

MULTICONTROL Power Supply/CPU Module Type B, 42 KByte Application Program Memory for Max. 42 K Instructions, Processing Time¹⁾ Optional (1.7 msec or 2.5 msec/K Instructions), 7168 Registers, 800 Flags, Real-Time Clocks, RS485/RS232/TTY Application Interface

- M2NTCP63-0** for 24 VDC Input Voltage
- M2NTCP64-0** for 220 VAC Input Voltage
- M2PSCP65-0** for 120 VAC Input Voltage

OPERATING ELEMENTS



TECHNICAL DATA

NTCP6#

Rack M264	
Processor	MOTOROLA 6809
Processing Time ¹⁾ Optional	
2 MHz	2.5 msec/K Instructions
3 MHz	1.7 msec/K Instructions
Registers	7168
Remnant	7148
Non-Remnant	20
Flags	800
Remnant	300
Non-Remnant	500
Application Program Memory	42 KByte RAM (for Max. 42 K Instructions) On Module, PROM Module Not Included.
Reset Button	NO
Status Display	NO
Time/Date	Real-Time Clock, Nonvolatile
Number of I/O	
Digital	264
Analog	80
Serial Interfaces	
On-line Interface	TTY (62.5 kBaud)
Application Interface	RS485/RS232/TTY
Hardware Timers	88
Software Timers	64
Timing Pulse/Timing Cycle	10 msec, 100 msec, 1 sec, 10 sec
Power Consumption	
At +8 V	4.0 W
At +15 V	2.0 W
At -30 V	-
Documentation	Hardware-Manual MULTICONTROL
English	MAHWMULTI-0
English	MAHWMULTI-E
French	MAHWMULTI-F
Italian	MAHWMULTI-I
Spanish	MAHWMULTI-S

PROGRAMMING

Programming the NTCP6# is done with the B&R PROgramming SYSTEM. Powerful standard function blocks are used for creating programs. The B&R PROgramming SYSTEM and standard software packages are described in section A7 "PLC Programming".

The PROM application program memory module is not included with the NTCP6# CPU and must be ordered separately. A description of the PROM application program memory module can be found in section "Application Program Memory Modules".

¹⁾ With revision 59.xx of type B CPUs, the processing time can be switched from 2.5 msec/K to 1.7 msec/K instructions.