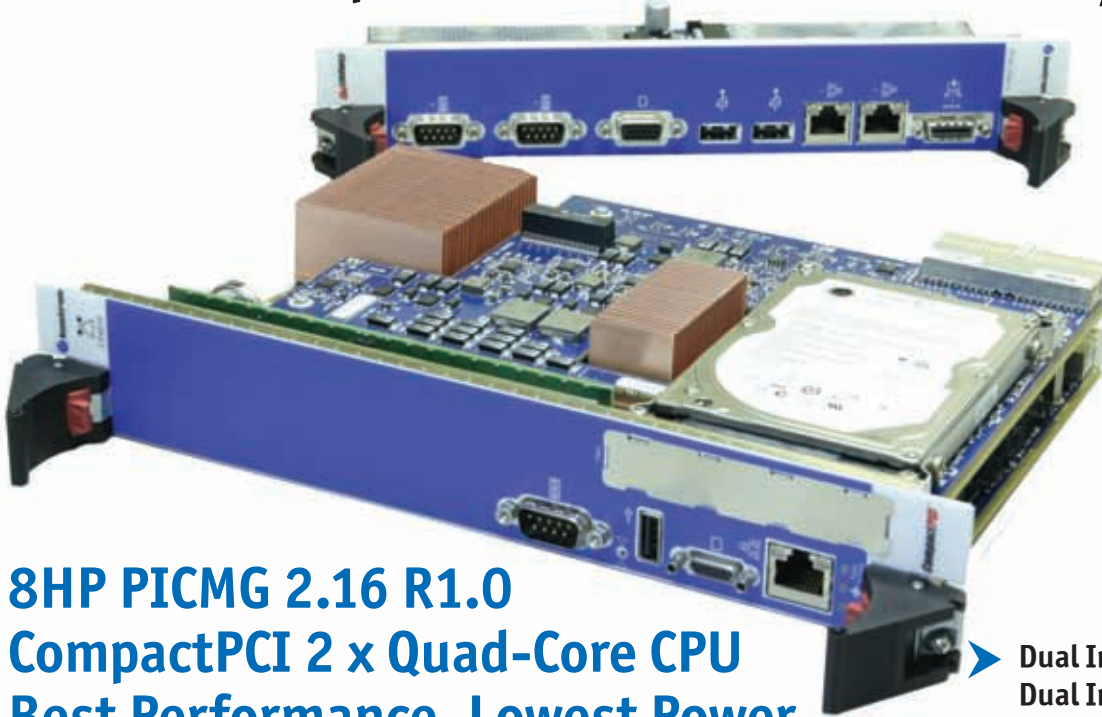




CP6014

Dual Intel® Quad-Core LV Xeon®
6U CompactPCI Processor Board, Rear I/O



8HP PICMG 2.16 R1.0 CompactPCI 2 x Quad-Core CPU Best Performance, Lowest Power

The CP6014 CompactPCI processor board featuring **two Quad-Core Intel® Xeon LV L5408 Processors**, complemented by the cost-optimized Intel® 5100 MCH chipset & Intel® I/O Controller Hub 9R. The significance of eight processor cores on one board now opens up market opportunities that never before existed and, likewise, gives new and existing Kontron CompactPCI customers tremendous system upgrade opportunities.

The Kontron CP6014 CPCI processor board targets storage, wireless infrastructure, security, voice, and the medical market segments. Taken in conjunction with the advantages of Virtualization software, the CP6014 can now address new data-intensive applications such as video recognition and medical imaging.

The Intel® Quad-Core Xeon LV L5408 Processor interfaces to the baseboard via the 771 land socket and is based on 45nm technology, which reduces power consumption, increases switching speed, and significantly increases transistor density over the previous 65nm technology. The L5408 has a power envelope of 40W and supports a core frequency of 2.13GHz, a Front Side Bus of 1066MHz, and an on-die L2 cache of a 12 MB (2x6 MB).

The Kontron CP6014 is also available with an 8HP CPCI Rear Transition Module. This RTM-CP6014 provides additional connectivity to the CPU front blade, and offers options for two SAS or SATA hard drives.

- Dual Intel® Quad-Core Xeon®; Dual Intel® Dual-Core Xeon®
- Intel® 5100 MCH chipset & Intel® I/O Controller Hub 9R
- 2.13GHz core frequency (45nm), 1066MHz FSB, and on-die L2 cache of 12 MB (2x6 MB)
- 16GB DDR2 memory (4 DIMMs sockets) with IOAT DMA for fast data transfer
- Solid State Drive (SSD) on USB, with 1GB, 2GB, or 4GB
- 4 SATA/SAS ports (onboard mezzanine & rear I/O)
- 2 COMs RS232 (1 front and 1 rear; or, 2 rear)
- 3 USB 2.0 ports (1 front and 2 rear I/O)
- Support for IPMI V1.5

Technical Information

Processors	<ul style="list-style-type: none"> - 2 x Intel® Quad-Core Xeon® L5408 Processor (8 cores total); or 2 x Intel® Dual-Core Xeon® LV 5138 (4 cores total) - Core frequency up to 2.13 GHz - 45 nm technology (L5408), 65nm (L5138), and 771 LGA
Cache Memory	- On-die L2 cache of up to 12 MB (2x6 MB) for L5408; up to 4MB for LV 5138
Chipset	- Intel® 5100 MCH chipset & Intel® I/O Controller Hub 9R
Bus interface	- Front Side bus of 1066MHz
Expansion slots	<ul style="list-style-type: none"> - XMC/PMC or onboard SATA HDD - Local PCI-X 64-bit / 133MHz on PMC-slot or a PCIExpress X4 on the XMC
System Memory	<ul style="list-style-type: none"> - Supports up to 16GB DDR2 memory (4 DIMMs sockets) at 667MHz - IOAT (I/O Acceleration Technology) DMA for fast data transfer
CompactPCI Bus Interface	<ul style="list-style-type: none"> - System bridge or stand alone operation - PCI 64/66 MHz; VIO 3.3V or 5V
Flash Memory	- Solid State Drive Module (SSD) at 1GB, 2GB, 4GB
I/O Connectors	<ul style="list-style-type: none"> - 1x 10/100/1000 Front and 2x 10/100/1000 available through the RTM or PICMG 2.16 - Three USB 2.0 ports (one at front and two at RTM) - VGA controller to support CRT on Front (VGA mini DSub15 connector) or Rear; 1 SATA mezzanine and 3 SAS/SATA RTM, or, 4 SAS/SATA RTM; 1 x COM port front and rear; or two COM ports at rear
Safety / EMC	<p>The board meets the following requirements:</p> <ul style="list-style-type: none"> - UL 60950, 3rd edition* ; CSA-C22.2 No 60950-00* ; IEC 60950-1: 2001* ; FCC 47 CFR Part 15, class B (USA); EN55022, Class B (Europe); EN55024 (Europe); EN60950-1* * Designed to meet or exceed.
Target Certifications	- NEBS Level 3 (designed for)
VGA Interface	- AMD ATI M72 VGA controller on PCI-E 4x bus; CRT only
Ethernet Controller	<ul style="list-style-type: none"> - 1 dual PCI Express Gigabit Ethernet controller 82571EB (Ophir) - 1 Gigabit Ethernet (controller integrated into ICH-9 using external GPHY 82566) - 1 ports front and two ports rear I/O
Warranty	- Two years limited warranty
Power Requirements	<p>CP6014 equipped with 4GB memory:</p> <ul style="list-style-type: none"> - Dual Dual Core LV 5138 2.13 GHz: max 126 W on 2 slots - Dual Quad Core L5408 2.13 GHz: max 156 W on 2 slots <p>Note: These absolute maximum values are obtained using Maxpower and Memtest, but should be considered impossible to attain in real-life applications. A more realistic maximum should be estimated to be at 80% of these values.</p>

BIOS	<ul style="list-style-type: none"> - Recovery Boot Block Flash BIOS. - Field-updateable BIOS for Linux OS - LAN Boot (flexible use of PXE-Boot or EtherBoot) - Video-less operation - Quick boot support (fast memory initialization of <5-7sec) - Parameter saving in non-volatile RAM - Support of Ethernet all speed grades (LAN Boot, front IO, rear IO) - Support of Watchdog (with dual stage NMI/Reset; with determination of origin of reset), - Support of USB (USB2.0: Keyboard, Mouse, Floppy, CD-ROM, HDD) - Support of Serial Ports (RS232, serial console redirection) - Support of VGA (CRT) - Support of the onboard RTC - Support of Boot Devices (USB-Flash drive, USB-memory stick, LAN, SAS-SATA Devices) - Setup console redirection to serial port (VT100 mode) with CMOS setup access - IO-APIC integration (for Linux required), ACPI-compliant 																		
IPMI Features	<ul style="list-style-type: none"> - Extensive Management Controller compliant to IPMI v1.5, PICMG 2.9 and design to meet PICMG 2.50 - Standard IPMI Watchdog (with pre-timeout interrupt) with reset/power cycle action - Standard IPMI host interface (interrupt driven KCS interface) - Serial over LAN (provides LAN access to BIOS menu) and IPMI over LAN (provides remote control such as power down/cycle) - Extensive sensors monitoring and external IPMB event generation on threshold, including overheat alarms - Standard IPMI System Event Log (SEL) for external or internal events - IPMI firmware is field updatable with fail safe rollover capability compliant to PICMG HPM.1 - Redundant IPMB communication path using cPCI IPMB1 																		
OS Compatibility	<ul style="list-style-type: none"> - Linux Kernel 2.6 - Windows Server 2003 																		
Physical	- Weight: 1,24 kg; 2.75lbs																		
Reliability	<ul style="list-style-type: none"> - MTBF >150,000 hours @ 30°/86°F (Telcordia SR-332, issue 1); no hard-disk 																		
Environmental	<p>ETSI EN 300 019-2-1 - storage environment tests ETSI EN 300 019-2-2 - transport environment tests ETSI EN 300 019-2-3 - operational environment tests Telcordia GR-63 - Environmental tests for central offices</p> <table border="1"> <thead> <tr> <th></th> <th>Operating</th> <th>Storage and Transit</th> </tr> </thead> <tbody> <tr> <td>Temperature*:</td> <td>0°C to +55°C/32°F to 131°F Standard Dual Dual Core L5138 2.13GHz (with forced airflow)</td> <td>-40 to 70°C / -10 to 158°F</td> </tr> <tr> <td>Humidity*: (non-condensing)</td> <td>5% to 93% @40°C / 104°F</td> <td>5% to 95% @40°C / 104°F</td> </tr> <tr> <td>Altitude*:</td> <td>4 000m / 13,123 ft Note: may require additional</td> <td>15 000m / 49,212 ft cooling above 1800m (5905ft)</td> </tr> <tr> <td>Shock*:</td> <td>3G each axis</td> <td>18G each axis</td> </tr> <tr> <td>Vibration*:</td> <td>5-200Hz. 0.2G, each axis</td> <td>5 Hz to 20 Hz @ 1 m2/s3 (0.01 g2 /Hz) (flat) 20 Hz to 200 Hz @ -3 dB/oct (slope down)</td> </tr> </tbody> </table>		Operating	Storage and Transit	Temperature*:	0°C to +55°C/32°F to 131°F Standard Dual Dual Core L5138 2.13GHz (with forced airflow)	-40 to 70°C / -10 to 158°F	Humidity*: (non-condensing)	5% to 93% @40°C / 104°F	5% to 95% @40°C / 104°F	Altitude*:	4 000m / 13,123 ft Note: may require additional	15 000m / 49,212 ft cooling above 1800m (5905ft)	Shock*:	3G each axis	18G each axis	Vibration*:	5-200Hz. 0.2G, each axis	5 Hz to 20 Hz @ 1 m2/s3 (0.01 g2 /Hz) (flat) 20 Hz to 200 Hz @ -3 dB/oct (slope down)
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