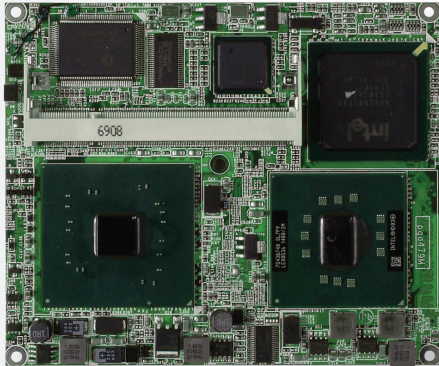


# EmXTX-i9150

Intel Celeron M 1.0GHz XTX™ CPU Module with CRT/LCD, Audio & LAN



◀ **New!!**



## Features

- **XTX Core Module**
- Onboard Celeron M 1.0GHz or Socket 478 FSB400/533MHz Intel Pentium M/ Celeron M CPU
- Support DDRII 400/533MHz SO-DIMM up to 1GB
- Support CRT, 18/36 bit LVDS
- Intel 82551QM 10/100 Base-T Ethernet
- Support 2 x SATA, 6 x USB 2.0 Ports, 2 x Serial Ports
- Support 4 x PCIe x1 Lane and 4 x PCI Slots

## System

<b>CPU</b>	Onboard Intel Celeron M 1.0GHz zero cache processor (FSB400MHz)
<b>Memory</b>	1 x 200-pin SO-DIMM up to 1GB DDRII SDRAM
<b>Chipset</b>	Intel 915GME + ICH6M
<b>BIOS</b>	Phoenix-Award PnP Flash BIOS
<b>ATA/IDE</b>	2 x Serial ATA (150MB/s) 1 x ATA100/66/33 port, support 2 IDE drives
<b>Watchdog Timer</b>	255-level Reset

## I/O

<b>Serial Port</b>	2 x RS-232 ports
<b>Parallel Port</b>	SPP/EPP/ECP mode
<b>Floppy</b>	Floppy shared with parallel port
<b>IrDA</b>	1 x SIR IrDA 1.1 compliant
<b>USB Port</b>	6 x USB 2.0 compliant
<b>KB/MS</b>	1 x PS2 K/B and Mouse
<b>Expansion Bus</b>	Support 4 x PCIe x1 lane and 4 x PCI slots

## Ethernet

<b>Chipset</b>	Intel 82551QM 10/100 base-T Ethernet
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## Audio

<b>Codec/Interface</b>	ALC655 AC97 Codec, support Mic-in/Line-in/Line-out
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## Display

<b>Graphics Chipset</b>	Integrated with Intel® Graphics Media Accelerator (GMA900)
<b>Graphics Interface</b>	CRT support up to 2048 x 1536 LCD support 18/36 bit LVDS up to 1600 x 1200

## Mechanical & Environmental

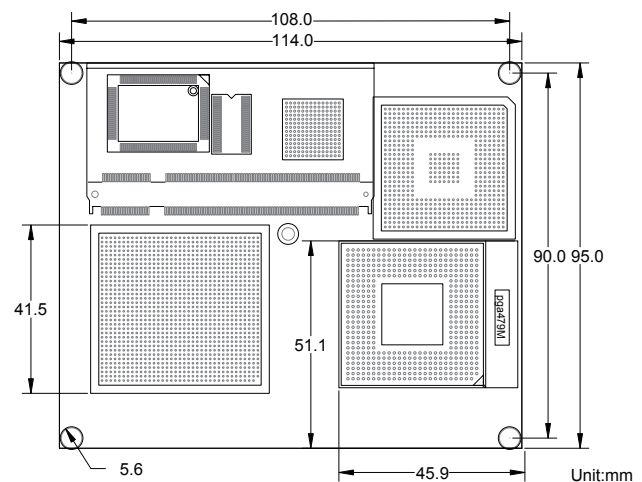
**Power Consumption** 5V/5.92A Max. (Pentium M 1.7GHz)

**Operating Temp.** 0 ~ 60°C (32 ~ 140°F)

**Operating Humidity** 0% ~ 90% (non-condensing)

**Dimension (L x W)** 114 x 95 mm (4.5" x 3.7")

## Dimensions



## Ordering Information

<b>EmXTX-i9150/CM1000</b>	Intel Celeron M 1.0GHz zero cache CPU FSB400/533MHz	
<b>EmXTX-i9150 (Optional)</b>	Intel Socket 478 Pentium M/Celeron M CPU FSB400/533MHz	
<b>PBE-1400</b>	XTX™ evaluation board in ATX form factor	Page 1-10
<b>Heatsink</b>	HS-9150-F1 (For BGA CPU onboard)	Page 5-4
	HS-9150-F2 (For PGA socket)	Page 5-4