

LVDS Transmitter and Receiver Modules

(applies to C&T 69K and S3 chipset)



Features:

- * Supports USB Ver. 2.0 on Flat Panel
- * Stand alone with Bracket

Chipset:

LVDS Receiver: DS90C386
LVDS Transmitter: DS90C385

Connectors:

Transmitter Board

26-pin SCSI connector 1.27 mm pitch
2 x 25-pin pitch 2.0 header
20-pin Hirose DF14A-20P-1.25H
30-pin Hirose DF13-30DP-1.25 C

Receiver Board

26-pin SCSI connector 1.27 mm pitch
2 x 25-pin pitch 2.0 header
+5V and +12V power connector

Display Type:

Support VGA, SVGA, XGA and Single/Dual pixel SXVGA

Ordering Information

FCDB-1521-18
LVDS 18/24-bit Transmitter
FCDB-1521-36
LVDS 36-bit Transmitter
FCDB-1522-18
LVDS 18 / 24-bit Receiver
FCDB-1522-36
LVDS 36-bit Receiver
LVDS-CXX:
SCSI 26-pin LVDS cable (length: 2M)

Other

Em104P-POST



Feature:

- * Em104P-POST could display the BIOS POST (Power-On-Self-Test) code when system boot up Power indicator show the bus power (included -5 V, +5 V, -12 V, +12 V, +3.3 V)
- * RESET indicator

Interface:

- PC/104 extension
- PC/104 Plus extension
- ISA interface
- PCI interface

Ordering Information

Em104P-POST
Debug card with ISA, PCI, PC/104 interface and 5 Level Voltage indicator
Em104P-POST/plus
Debug card with ISA, PCI, PC/104 Plus interface and 5 Level Voltage indicator.
Em104P-POST
Debug card with ISA, PCI, PC/104 interface and 5 Level Voltage indicator