

Advanced Packet Optimizing card to enhance your x86 server with MIPS multi-core power



SPECIFICATION

Processor	Cavium Octeon™ CN38XX series Multi-core MIPS6 SoCs		
Form Factor	PCI-X board in proprietary length		
System Memory	- Two 244pin Mini-DIMM sockets, support DDR2/667 up to 8GB		
	 Onboard RLDRAM up to 288MB for pattern matching 		
Ethernet Port	Four RJ45 Gigabit Ethernet ports		
Bypass Segment	Two Gen-2 bypass segemtns onboard		
Expansion	- One vertical SPI 4.2 interface for connection with 10G/1G modules		
	- Golden finger of 64bit/133MHz PCI-X interface for installation as a PCI-X add-on card		
Operating Environment	- Temperature: 5 to 40°C		
	- Humidity 5% to 95%RH		
Storage Environment	- Temperature: -20 to 70°C		
	- Humidity 5% to 95% RH		

FEATURE

- Tremendous packet processing power with up to 16 MIPS64 processor cores
- Rich built-in security features: compressiondecompression, encryption-decryption, regular expression, pattern matching, TCP-offload, SSL, IPS, Antivirus and Qos
- Supports both Host Mode for stand-alone system board and Target Mode as a PCI-X add-on card
- Four Copper GbE ports with two Gen.-2⁽¹⁾ bypass segments
- Flexible SPI 4.2 interface for variable application expansion
- Optional onboard RLDRAM for high performance pattern matching
- Two Mini-DIMM sockets support ECC registered DDR2 up to 8GB
- Onboard CF-socket for Type-I/II CF card

ORDERING GUIDE

Part No.	Cavium Processor	MIPS64 Cores		Bypass Segment	RLDRAM
RF-330	CN3860-500	16	4	1	288MB

⁽¹⁾Gen.-2.0 bypass: The latest bypass generation with software programmable Open/Bypass mode by power failure and