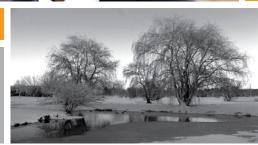


FlexPacket ATCA-PP50

- AdvancedTCA Blade for Deep Packet Inspection
- Ideal for Content-Aware Routing & Security Applications







Targeting the needs of 3G Wireless and IP Multimedia Subsystem (IMS) markets, FlexPacket™ ATCA-PP50 provides telecom equipment manufacturers with deep packet inspection capabilities to support advanced content-aware routing and security functions required by multi-service IP networks. The blade is engineered to meet the application needs of next generation infrastructure including IPTV, radio network controllers (RNC), security gateways, session border controllers, WiMAX base station aggregation, and wireless xGSNs.

The PP50 provides superior performance via one or two discrete multi-core MIPS64 packet processors. Each processor provides 8 multi-threaded cores and contains a built-in security co-processor capable of handling up to 10Gbps of bulk encryption / decryption (20Gbps per blade). The PP50 supports each processor with up to 8GB of memory (16GB per blade) as well as access to a TCAM and content-based processors via mezzanines. TCAM is especially important for very high performance IPv6 routing platforms.

The PP50 interconnects the processors, I/O, and backplane fabrics using a non-blocking 10GbE switch. Each RMI XLR732 processor is provided with two 10GbE ports to the switch providing full duplex 10GbE capabilities. External I/O is supported over a dual redundant 10GbE backplane fabric (PICMG 3.1.9) as well as through direct connection to 10GbE and 1GbE ports on the front or rear depending on a customer's specific cabling requirements.

Coupled with Continuous Computing's FlexTCA Systems and Trillium® protocol software, the PP50 provides the fastest path from application development to deployment revenue. From IPTV to Wireless Core Networks, the PP50 is the blade of choice for deploying a wide range of high-performance, scalable telecom applications.

FEATURES + BENEFITS

PROCESSING

- Dual processor XLR732 multi-core MIPS64 processor
- Eight multi-threaded cores per XLR improves code efficiency and developmen
- Internal fast messaging network improves core efficiency and reduces latency
- Integrated 10Gbps security acceleration engine to maximize security performance
- Mezzanines for TCAM or specialized off-load engines (including dedicated user-specific ASIC / FPGA)

MEMORY & STORAGE

- Up to 8GB per CPU (16GB per board) support for 667 / 800MHz DDR2
- TCAM support via mezzanine
- On-board CompactFlash and persistent SRAM

CONNECTIVITY

- Base interface compliant to PICMG 3.0 (dual redundant 1GbF)
- Fabric interface compliant to PICMG 3.1, option 9 (10GbF)
- 2 x 10GbE front panel network
- RTM with 2 x 10GbE or 10 x 1GbE

FlexPacket ATCA-PP50

SPECIFICATIONS

CPU / MEMORY

- 1 or 2 RMI XLR732 processors
- 1GHz multi-core MIPS64, 8 multi-threaded cores (32 virtual cores)
- 2MB of 8-way set associative L2 cache
- 2GB, 4GB, or 8GB DDR2 memory per processor

I/O

- Dual redundant 1GbE base interface
- 10GbE fabric interfaces (PICMG 3.1.9)
- 2 x 10GbE on front panel
- RJ45 1GbE front panel management port

EXPANSION OPTIONS

- 36Mbit or 72Mbit TCAM mezzanine using Netlogic NL71024
- Optional 1Gb or 2Gb CompactFlash per processor
- Rear Transition Module
 - 2 x 10GbE (SFP+)
 - 10 x 1GbE (SFP)

SOFTWARE

- Wind River Linux PNE (Carrier-Grade Linux)
- RMI Operating System (Native C-based OS for fast path)
- Network and routing protocols including fast path and slow path functions
 - Includes IPv4/6 forwarding, IPSEC, Tunneling, GRE, etc.

MECHANICAL & ENVIRONMENTAL COMPLIANCE

- Standard 8U single-slot (6 HP) front board and RTM
- Operating environment:

 - Temperature: 0.0 to +55C
 Humidity: 5%-80% (non-condensing)
 Vibration: 20Hz-2KHz random multi-axis, 0.5G RMS
- Storage/transportation environment:
- Temperature: -40C to +85C Humidity: 5%-95% (non-condensing)
- Vibration: 20Hz-2KHz random multi-axis, 6G RMS
- Assembly components meet UL 94-V0 flammability rating

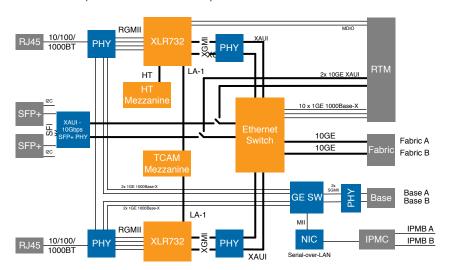
PLANNED CERTIFICATIONS

- UL 60950-1 Safety of Information Technology Equipment
 IEC/EN 60950-1 Safety of Information Technology Equipment
 FCC Part 15, Subpart B, Class A

- CE Mark Meets EMC directive 89/336/EEC

 Designed Telcordia NEBS GR-63-CORE and GR-1089-CORE Level 3
- RoHS 6 / 6 Compliant

FLEXPACKET ATCA-PP50 BLOCK DIAGRAM (ETHERNET CONNECTIVITY)











Global Headquarters Continuous Computing

9450 Carroll Park Drive San Diego, CA 92121 USA T +1.858.882.8800 F+1.858.777.3388 info@ccpu.com

©2008 Continuous Computing Corporation.

Continuous Computing, the Continuous Computing logo, Create | Deploy | Converge, Flex21, FlexChassis, FlexCompute, FlexCore, FlexDSP, FlexPacket, FlexStore, FlexSwitch, FlexTCA, Network Service-Ready Platform, Quick!Start, TAPA, Trillium, Trillium+plus, and the Trillium logo are trademarks or registered trademarks of Continuous Computing Corporation