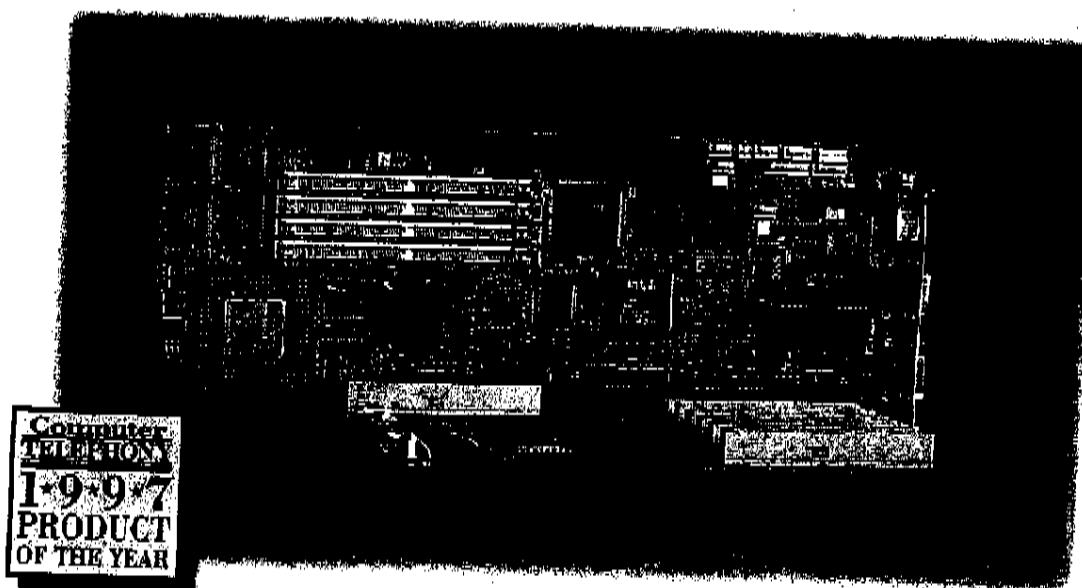


## PENTIUM TRITON-II WITH SVGA VIDEO



### SLOT-SAVING ON-BOARD VIDEO WITH PENTIUM TRITON-II SPIED

With on-board video and SCSI-2 interfaces, you'll save slots. Plus, you won't have to buy additional boards, worry about compatibility, or deal with extra vendors. The Trenton TR-T2VPCI single board computer is all you'll need for fast, worry-free industrial PC performance.

And, with on-board PCI Super VGA (full-screen, full-motion, up to 30 frames per second), you have access to the most richly featured video solution.

With faster PCI bandwidth (100+MB/second sustained) and up to 233MHz processing speed, this Pentium Triton-II (430HX) single board computer gives you today's highest Pentium performance processor board for your vital systems applications.

And, as a founding member of PICMG, we guarantee the TR-T2VPCI complies with industry requirements (like the PCI Local Bus standard).

It's flexible — you can depend on it. And it's fast — always. It's Trenton.



#### CPU:

Intel Pentium 166MHz  
Intel Pentium MMX 200/233MHz

#### BUS INTERFACES:

ISA and PCI Bus compatible

#### DATA PATH:

Data/Memory	-	64-bit
ISA	-	16-bit
PCI	-	32-bit
Video	-	64-bit

#### BUS SPEED:

ISA - 8MHz  
PCI - 33MHz

#### DMA CHANNELS:

Fully PC compatible with seven DMA channels, each supporting type F transfers.

#### INTERRUPTS:

Fully PC compatible with interrupt steering for PCI Plug and Play compatibility.

#### BIOS (FLASH):

AMI Hi-Flex BIOS with built-in advanced CMOS setup for system parameters, peripheral management for configuring on-board peripherals, PCI-to-PCI bridge support, and PCI interrupt steering. Supports flash devices for BIOS upgrading via Floppy interface. Custom BIOSs available.

#### CACHE MEMORY:

The 64-bit wide second level (L2) cache supports 512K direct mapped write-back cache policy with synchronous pipelined burst SRAMs.

#### DRAM MEMORY:

The DRAM interface is a 64-bit path that supports up to 512MB of either Fast Page Mode (FPM) or Extended Data Out (EDO/Hyper Page Mode) memory. Supports 60ns (optimal) or 70ns industry standard 36-bit wide gold finger SIMM DRAM in four 72-pin SIMM sockets. All 512MB is cacheable.

#### ERROR CHECKING AND CORRECTION/PARITY:

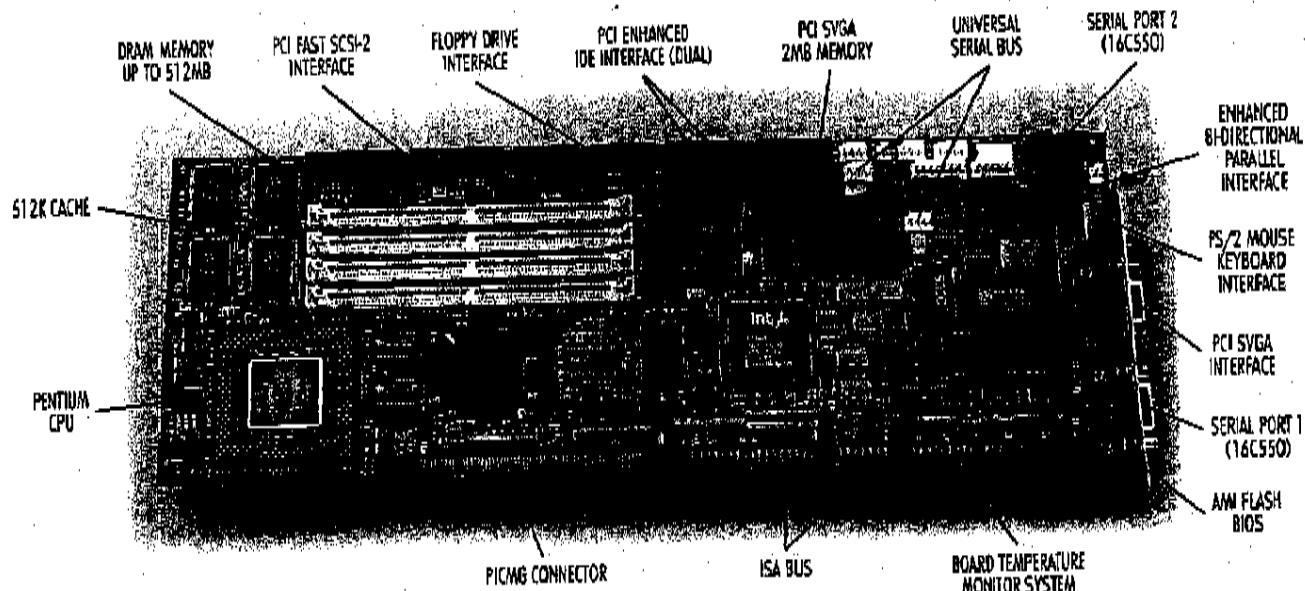
The memory interface includes parity checking and supports ECC mode (via BIOS setting) for single-error correction, double-error detection, and detection of all errors confined to a single nibble.

#### PCI LOCAL BUS INTERFACE:

Fully compliant with the PCI Local Bus 2.1 Specification. This board has optimized the PCI interface to allow the CPU to sustain the highest possible bandwidth (greater than 100MB/sec sustained) and low latency of the PCI Bus. Supports four PCI masters, pipelined snoop bypass feature, and improved PCI to DRAM write-back policy.

#### UNIVERSAL SERIAL BUS:

The Universal Serial Bus is an interface allowing for connectivity to many standard PC peripherals via an external port.



#### **PCI SUPER VGA INTERFACE:**

The Cirrus Logic GD5446 video interface is a PCI Local Bus device that supports pixel resolutions up to 1280 x 1024 non-interlaced, and 16.8 million colors at resolutions up to 1024 x 768. 2MB of on-board EDO display memory with 64-bit wide data path and 80MHz memory clock. Displays in full-screen, full-motion up to 30 frames per second, true color at 1024 x 768. Allows independent graphics and video streams to be displayed on-screen with true-color video and 256-color graphics. Software drivers for enhanced performance and resolution are available for the most popular operating systems.

#### **BTMS - BOARD TEMPERATURE MONITOR SYSTEM:**

The temperature monitor system provides a measurement of the temperature at the board surface of the single board computer and provides a readable register for current status.

#### **WATCHDOG TIMER:**

The software controlled watchdog timer monitors system activity and generates a reset pulse in the event of a timeout.

#### **PS/2 MOUSE INTERFACE:**

Compatible with a PS/2-type mouse. Jumper selectable for either on-board or panel mount PS/2 type connector.

#### **PCI FAST SCSI-2 INTERFACE:**

The SCSI interface is a PCI Bus Master device that supports fast SCSI data transfer up to 10MB per second and bursts data to the host at full PCI speeds. Enable/disable active termination is provided with terminator voltage protected by self resetting fuses. Software drivers are available.

#### **PCI ENHANCED IDE INTERFACE (DUAL):**

A high performance PCI Bus Master EIDE interface is capable of supporting up to four IDE Type 4 disk drives in a master/slave configuration. With LBA settings in the BIOS parameters, disk drives greater than 528MB are supported with transfer rates to 22MB per second.

#### **FLOPPY DRIVE INTERFACE:**

Supports up to two floppy disk drives in combinations of 360K to 2.88MB.

#### **SERIAL INTERFACE:**

Two high speed FIFO (16C550) serial ports with independently programmable baud rates. Each serial port has hardware selectable IRQs and BIOS selectable addressing. Filtered connector is provided to minimize FCC interference.

#### **ENHANCED BI-DIRECTIONAL PARALLEL INTERFACE:**

Compatible with PC/AT bi-directional parallel port and supports EPP and ECP modes. The ECP mode is IEEE 1284 compliant. Parallel port has hardware selectable IRQs and BIOS selectable addressing.

#### **KEYBOARD INTERFACE:**

Compatible with AT type keyboard and jumper selectable for either on-board PS/2 type connector or off-board cabling. Keyboard voltage is protected by a self-resetting fuse.

#### **POWER REQUIREMENTS:**

+5V - 3.95 Amps typical  
 +12V - <100 mAmps typical  
 -12V - <100 mAmps typical

#### **MTBF:**

77,000 POH (Power-On Hours) at 40° C., per MIL-HDBK-217F

#### **TEMPERATURE:**

0° - 60° C. Operating  
 -40° - 70° C. Storage

#### **ORDERING INFORMATION:**

Model Name	CPU Speed	Model #
TR-T2VPCI/233M/xM	233MHz/MMX	5449-19
TR-T2VPCI/200M/xM	200MHz/MMX	5449-18
TR-T2VPCI/166/xM	166MHz	5449-12

(xM = Memory)

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