

High-Density Digital I/O and Timer for StackableUSB™ USB3148



Features

- √ 53 user controllable TTL-level I/O lines
- ✓ Timers/counters, switches/LEDs
- √ 10-bit single-ended/differential A/D
- ✓ Two (2) RS232 serial ports, one (1) each I2C and SPI bus interfaces
- ✓ StackableUSB 2.0 device with stacktogether connectors for embedded systems or plug/cable configuration for remote applications



The USB3148 operates as a USB 2.0 Fullspeed device in a StackableUSB system or connected to a PC using a standard USB cable. Three (3) 82C55's provide 53 lines of bi-directional general system I/O. An on-82C54 provides board three (3)counter/timers. There are also two (2) RS232 serial ports, one (1) each I2C and SPI bus interface, a single comparator, and a 10-bit A/D converter. The USB3148 provides this wealth of system I/O on a small 3.55 x 3.755 footprint.

The USB interface, in the stackable or cable configuration, allows the USB3148 to be

added into any USB system by using the standard USB plug-and-play hardware and software configuration after installing the software provided for the host computer. This significantly simplifies user's system integration.

The USB3148 implements many features to enhance system reliability. Software programmable pull ups/downs on each I/O line provide deterministic reset levels. The USB3148 protects against power sequencing issues with other boards in the stack or system by providing current limiting resistors on all I/O lines.

Software Support

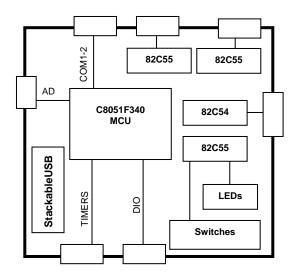
Windows XP Windows CE Linux

Compatible Hardware

StackableUSB Host single board computers or microcontrollers PC Host desktops and laptops

Mounting/Packaging

ENC104-4 Standoffs, STDOFFUSB



Technical Details:

Three (3) 82C55 digital I/O devices provide 53 lines of TTL I/O. Direction of the I/O signals is programmable in two (2) 8-bit groups and two (2) 4-bit groups for each 82C55. The 82C55 TTL I/O lines can source and sink at 2.5mA. An on-board 82C54 devices provides three (3) 16-bit timer/counters.

The USB3148 comes with pre-installed software that allows the transfer of data between the USB port and all of the onboard peripherals with no need to write additional code on the USB3148 device side of the system. To ease installation of the USB3148 on the host side, USB drivers and example code are supplied. source eliminating the need for prior USB experience. Host side operating systems supported are Linux, WindowsCE, and Windows XP.

A command and control protocol implemented over the USB interface allows direct communication with the on-board peripherals via inport and outport driver calls executed on the host computer. A custom interrupt service routine can be called directly by the host side USB driver. The USB3148 utilizes a microcontroller with a built-in USB device controller which acts as

the communication channel between the onboard peripherals and the StackableUSB host computer. The USB3148 is USB 2.0 compliant and supports both full-speed (12Mbps) and low-speed (1.5Mbps) transfer rates.

Other peripherals comina from the microcontroller include a single-ended or differential input A/D with a range of 0 to Conversions can be triggered from timers or from an external pin. There are two COM1 serial ports, and configured with RS232 transceivers with the TXD and RXD lines supported. Other system level functions included on the USB3148 are an interrupt controller and a watchdog timer.

The on-board microcontoller includes 64k of flash, and 4352 bytes of SRAM which allows users to write board specific control routines for the USB3148. Most instructions are executed in one or two system clock cycles, allowing the MCU to achieve performance as high as 48 MIPS. This feature can off load the host CPU of interrupt service routines, reserving the USB communication link for sending system level commands rather than directly controlling each event.

The StackableUSB bus architecture allows up to five (5) USB peripheral boards to be stacked above and/or below a single board computer, making ten (10) board systems with any combination of StackableUSB boards possible without the use of a hub. Please call Micro/sys Technical Sales for details.

Single-master, single-slave only

Clock speeds up to 12.5MHz

| | | Wa | atchdog Timer: |
|-------------------------------|---|-----------------------|--|
| Specifications: Mechanical: | | | Program must refresh watchdog timer periodically, or system will be reset |
| | | | Enabled through software |
| | PC/104 mounting holes 3.55" (plus I/O region) x 3.775" x .6" | De □ □ | ebug/Download Interface: Silicon Laboratories 2-wire C2 interface Allows programming of program flash or |
| Power Requirements: | | | debugging of user application |
| | +5V ±5% at 320mA max | | |
| Environmental: | | Digital I/O: | |
| | Operating range 0°C to +70°C | | Three (3) 82C55s provide 53 lines of TTL-level digital I/O, 2.5mA source/sink |
| | ET-version operating range -40° to +85°C -40° to +85°C storage | | Software-configurable pull up/downs on I/O for initialization after reset |
| | 5%-95% relative humidity, non-condensing | | 470-ohm current limiting resistors |
| | | | Grounds for all systems |
| Microcontroller Core Section: | | | • |
| | Silicon Laboratories 8051 48MHz clock rate | | ner/Counter: |
| | Optimized 8051 instruction set | | 82C54 provides three (3) 16-bit |
| _ | 64kbytes of flash memory | | timer/counters |
| | 4352bytes of SRAM | | Software-configurable in/external clock and cascading |
| USB Interface: | | Switch/LEDs: | |
| | USB 2.0 full-speed | <u> </u> | 8-position switch, eight (8) LEDs from |
| | USB device or function controller only | | dedicated 82C55 |
| | (Controller does not operate as a host) | | |
| Serial Ports: | | External Connections: | |
| | Two (2) async serial ports | | 14-pin header for COM1-COM2 |
| _ | RS232 levels | | 80-pin high density connector for digital I/O |
| | TXD and RXD signals supported for | | 26-pin header for timer, external-clock, external IRQ, additional digital I/O |
| | both ports | | 10-pin header for debug/download |
| | | | 34-pin header for MCU signals (ADC, |
| SMBus/I2C Port: | | | timers, DIO, I2C, comparators) |
| | Configurable as master or slave | | 2-pin removable terminal strip for power |
| | Clock speeds up to 4.8MHz | | |
| SPI Bus: | | | velopment Kit: |
| 35 | i Duo. | | Base module |

Analog Input:

200ksps

0 to 3.3V range

Complete cable set

Documentation, schematics, sample software

10-bit resolution, successive-

Conversion starts from timer or external pin

Single-ended or differential inputs

approximation register ADC

Ordering Information:

OEM Modules:

USB3148-ST High-Density DIO and

Timer board with

StackableUSB stackthrough connector

USB3148-ST-ET High-Density DIO and

Timer board with StackableUSB stackthrough connector; -40° to +85°C operating

temperature

USB3148-PC High-Density DIO and

Timer board with Mini-B USB connector for PC

connection

USB3148-PC-ET High-Density DIO and

Timer board with Mini-B USB connector for PC connection; -40° to +85°C operating temperature

CS3148 Complete cable set

Related Products:

CA4089 Breakout cable to two (2)

DB9 COM port connectors

BA4089 Breakout assembly to

two (2) DB9 COM port

connectors

TB50540 40-pin to 40-pin ribbon

cable with 50-point screw

terminal breakout board BA0034 34-pin to 34-pin ribbon

34-pin to 34-pin ribbon cable with 34-point screw

terminal breakout board

BA0028 26-pin to 26-pin ribbon

cable with 26-point screw

terminal breakout board

CA5049 50-pin to 50-pin ribbon cable

CA5050 34-pin to 34-pin ribbon cable

CA5051 26-pin to 26-pin ribbon cable

Cables nominally 15", other lengths available

Development Board Kits* DK3148-ST-ET High-Density DIO and Timer board with StackableUSB stackthrough connector, -40° to +85°C operating temp, Windows-ready development kit DK3148-PC-ET High-Density DIO and Timer board with Mini-B USB connector for PC connection; -40° to +85°C

operating temp, Windows-

ready development kit

*See Development Kit Specifications