PPAP-250 Communication Appliance Board Specification

#	Item	Specification
1	Memory Support	1 x 168-pin DIMM socket supports PC100/133 MHz and up to 512 SDRAM, NO ECC
2	Chipset	Intel 815E/ B-stepping
3	Processor	The M/B board supports uni-processor implementation for Intel [®] Celeron™ up to PIII processor and Tualatin™(0.13um PIII) 1.26GHz.
4	4 on board 10/100 Fast Ethernet port	1x internal Intel [®] 82562ET 3x Intel [®] 82559er
5	On-board LAN-ports LED	 Two build-in type LEDs on RJ-45 sockets. Left: ACT/LNK, green. Off - no connection. On - good linkage. Flashing - data accessing Right: 10/100, yellow. Off - data transfer rate at 10Mbps. On - data transfer rate at 100Mbps. The LED works only when the left one (for ACT/LNK) is on. Otherwise it should be off.
6	PCI Expansion	A Golden finger is designed for connecting to a Back Plane that consists of two PCI slots (ABP-03V2F) or one PCI and one PICMG slots (PBP-02V2A). The PCI Connecter fully compliant with PCI 2.1 spec with DC +/- 12V, 5v, and 3.3v.
7	Motherboard size	5.75" (146mm) W x 9.5" (241mm) L The board conforms with the 5.75" x 8.5" mounting holes.
8	Thermal diodes On Processor	The thermal diodes on the processor is supported for temperature read out via suitable application software and/or BIOS code
9	Power supply	 ⇒ AT type PSU. ⇒ 100~127VAC and 200~240VAC switch selectable input. ⇒ 50~60 Hz line frequency ⇒ Max. output: 100W.
10	USBs	One USB channels with pin connector for dual USB ports.
11	IDE channels	Two IDE channels, one 40-pin and one 44-pin. The 44-pin IDE connector (2mm pitch) supports PNA-CFB card (Compact flash adaptor board)
12	LEDS connectors	8 user programming LED board driven from GPIO pins on the front of the M/B via Pin connectors.
13	VGA	An integrated VGA pin connector for holding the VGA adapter card (PNA-V100).
14	EIA/TIA232	2 COM ports on Board, one is 2x5 pin connector and the other is DB9 for system console
16	MiniPCI interface	A MniPCI pin socket (Type IIIA) supports MiniPCI module. ie. modem
17	Watch Dog Timer	The WDT supports 255 intervals from 0.5 sec/ Min to 254.5 sec/ Max by s/w programming. WDT time-out will issue H/W reset signal to reset the system

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18	System Monitoring	CPU & System Temperature, FAN RPM, and operating voltage
19	PCB-thickness	It shall follow PICMG norm: T= 62^{+8}_{-5} mil (1.57 $^{+0.20}_{-0.13}$ mm)

