

## μQseven Rel. 2.0 Compliant Module with Freescale™ i.MX6 Processor

### DESCRIPTION

μQ7-i.MX6 is a μQseven Rel. 2.0 Compliant Module designed by SECO that offers top computational and graphical performances given by low-power consuming ARM architecture.

The board integrates i.MX6 Multimedia Applications platform from Freescale™, a multimedia processor integrating a multicore Cortex™ A9 ARM Core (up to Quad Core processors), ideal for applications requiring multimedia capabilities and/or high levels of parallel computing.

All this comes in the brand new form factor, μQseven, just 40x70mm, introduced with the latest Qseven® specifications Rel. 2.0.

### TECHNICAL SPECIFICATIONS

<b>Processor</b>	Freescale™ i.MX6 Family, based on ARM Cortex-A9 processors: Single core (i.MX6S) up to 1GHz Dual Core (i.MX6D), Dual Core Lite (i.MX6DL) up to 1.2GHz Quad Core (i.MX6Q) up to 1.2GHz clock
<b>Memory</b>	up to 2GB DDR3 onboard (up to 1GB with i.MX6S)
<b>Graphics</b>	Integrated graphics, each processor provides up to 3 separated accelerators for 2D, OpenGL® ES2.0 3D and OpenVG™ (OpenVG™ accelerator only available with i.MX6D and with i.MX6Q) Supports up to 4 independent displays (only up to 2 displays with i.MX6DL and i.MX6S) HDMI interface 1 x Dual Channel or 2 x Single Channel 18/24 bit LVDS interface
<b>Resolution</b>	LVDS, up to 1600 x 1200 HDMI, up to 1080p
<b>Mass Storage</b>	1 x SATA interface (only with i.MX6D and i.MX6Q) eMMC soldered onboard MMC/SD/SDIO interface
<b>PCI-Express</b>	1 x PCI-Express x1 lane
<b>USB</b>	1x USB OTG 4x USB 2.0 Host
<b>Ethernet</b>	Gigabit Ethernet interface
<b>Audio</b>	I2S / AC'97 Audio Interface
<b>Serial ports</b>	2 x serial ports CAN Bus interface
<b>Interfaces</b>	SM Bus SPI I <sup>2</sup> C bus 8x GPIO/S
<b>Embedded additional RTC circuitry for lowest power consumption</b>	
<b>Dimensions</b>	40x70 mm (μQseven, 1.57" x 2.76")

### ACCESSORIES

Heat Spreader and heat sink kits available on request.

### ORDERING INFORMATION

Please Contact SECO for Ordering Information

