

APci7001

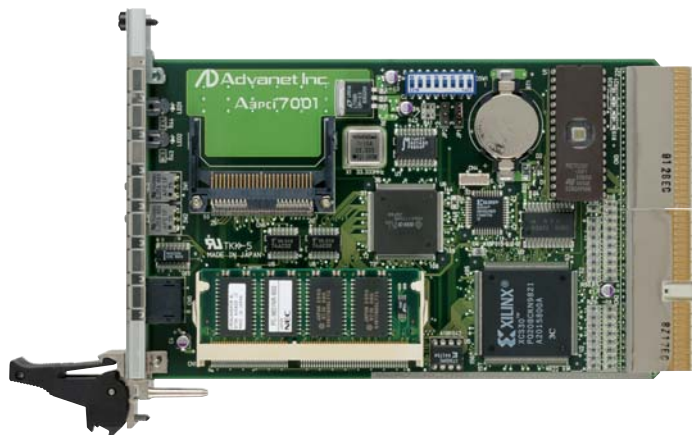
SH-3 CPU Board



SH-3 CPU Board

Features

- SH-3 CPU built-in serial port
- Advanet original SH-3 to PCI bus bridge built-in
- High-speed SRAM accessible from PCI bus master for combination with LAN boards with a bus master function
- Compact 3U size, single slot (4TE) width
- Low power consumption
- Hitachi 32bit RISC CPU SH7708R operates at 100MHz
- Built-in 8KByte cache, integer arithmetic peak value of 100 MIPS
- Built-in real-time clock (battery backup)
- CompactFlash socket (3.3V IDE mode)
- SO-DIMM socket (Up to 64MBytes)
- Jumper post for ROM-ICE
- HotSwap support



Specifications

CPU	Hitachi 32bit RISC CPU SH7708R (100MHz)
Memory	EPROM One 32-pin DIP socket (Up to 1MByte) SRAM Accessible from 256KByte 20ns CPU and PCI bus master SO-DIMM socket 144-pin, 3.3V voltage, supports EDO types with an access time of 60ns or less (SO-DIMM is optional)
CompactFlash	3.3V module support
Onboard I/O	One RS-232C serial port (RJ-45 connector)
Real time clock	Battery backup
HotSwap support	High Availability Platform (system slot)
Bus interface	PICMG 2.0 R2.1 CompactPCI Specification compliant 33.3MHz bus clock 32-bit data bus 5V signaling voltage Four interrupt pins (INTA, INTB, INTC, INTD) Permissible number of bus masters: 7
Power requirements	DC5V±5%
Board size	160mm x 100mm x 20mm 3U size single slot width (excluding protrusions such as handle connectors)

Models

A3pci7001: SH-3 CPU board

A3pci7001-VX: BSP for VxWorks

* SO-DIMM and CompactFlash are optional in all models.



HUMAN ELECTRONICS

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Note: The following specifications and product appearance are subject to change for enhancement without notice.

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ISO9001
Certification: No.4016-1995-AQ-KOB-RvA

ISO14001
Certification: No. EMSC-1426