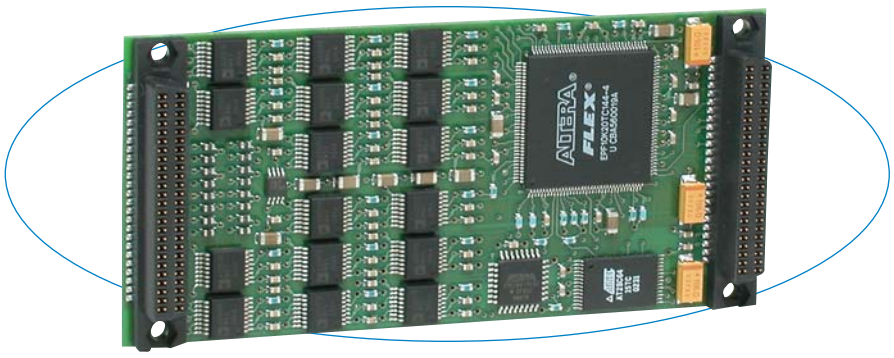
Acromag
THE LEADER IN INDUSTRIAL I/O

IP220A-x
12-Bit D/A, Analog Output



The IP220A features individual D/A converters on each channel for better performance.

The IP220A outputs analog voltage signals to drive up to 16 devices. When used with a carrier that holds four IP modules, up to 64 voltage outputs can be obtained from a single card cage slot.

Each output channel has its own 12-bit D/A converter (DAC). Individual DACs are faster, and they eliminate glitches typically caused by the re-acquisition process of sample and holds found on multiplexed output boards.

Individual channels also have double-buffered data latches. You can select to update each output when it is written to, or to update all outputs simultaneously. Simultaneous outputs better simulate linear movements in motion processes.

Features

- 8 or 16 analog voltage output channels
- Independent 12-bit D/A converters per channel with an 8.0µS settling time
- Bipolar voltage (non-isolated) outputs: -10 to +10 volts
- Double-buffered DACs
- High load capability (5mA output current)
- Built-in calibration coefficients

Benefits

- Outputs reset to 0 volts.
- Internally stored calibration coefficients ensure accuracy.
- Software provides easy selection of transparent or simultaneous output modes.
- Double-buffered DACs allow new data to be written to each channel before the simultaneous trigger updates the outputs.

Specifications

Analog Outputs

Output configuration: 8 or 16 single-ended.

D/A Resolution: 12 bits.

Output range: Bipolar, -10 to +10V.

Settling time: 11µS.

Maximum throughput rate:
Outputs can be updated simultaneously or individually.
One channel: 11µS/conversion.
Sixteen channels simultaneously: 17µS/16 channels.

System accuracy: 0.025% of 20V span maximum corrected error (i.e. calibrated) at 25°C with the output unloaded.

Data format (left–justified): Bipolar Offset Binary.

Output at reset: 0 volts.

Output current: -2 to +2mA (maximum). This corresponds to a minimum load resistance of 5K ohms with a 10V output.

Short circuit protection: Indefinite at 25°C.

IP Compliance (ANSI/VITA 4)

Meets IP specifications per ANSI/VITA 4-1995.

IP data transfer cycle types supported:
Input/output (IOSel*): DAC data, control registers, DAC offset and gain calibration coefficients.
ID read (IDSel*): 32 x 8 ID PROM.

Access Times (8MHz clock):
ID EEPROM read: 0 wait states (250nS cycle).
DAC channel data write: 1 wait states (375nS cycle).
DAC offset/gain coeff. read: 1 wait states (375nS cycle).
Control register access: 1 wait states (375nS cycle).

Environmental

Operating temperature: 0 to 70°C (IP220-8/16) or -40 to 85°C (IP220-8E/16E models).

Storage temperature: -55 to 100°C (all models).

Relative humidity: 5 to 95% non-condensing

MTBF: Consult factory

Power: +5V: 200mA.
+12V from P1 or +15V from P2: 300mA.
-12V from P1 or -15V from P2: 180mA.

Ordering Information

Industry Pack Modules

IP220A-8
Eight voltage outputs

IP220A-8E
Same as IP220-8 plus extended temperature range.

IP220A-16
Sixteen voltage outputs

IP220A-16E
Same as IP220-16 plus extended temperature range.

For Industry Pack Carrier Cards, see Bulletin 8400-139.

Software

IPSW-API-VXW
VxWorks® software support package

IPSW-API-QNX
QNX® software support package

IPSW-API-WIN
Windows® DLL driver software support package

IPSW-LINUX
Linux™ support (website download only)

For more information on Industry Pack Carrier Cards, software support and hardware accessories, please see Bulletin 8400-139.

Acromag, Inc. • PO Box 437, Wixom, MI 48393 • Phone: 248-624-1541 • Fax: 248-624-9234 • e-mail: sales@acromag.com • www.acromag.com

Printed in USA 7/03 Copyright © 2003 • Data subject to change without notice. • All trademarks are the property of their respective owners.