ROM Programmers

Programming for Up to 10, 8-Mbit PROMs

R4952

- Can be Used for 16-Mbit PROMs
- High-Speed Programming Algorithm
- Reduction in Function Execution Time
- Standard Serial I/O and Parallel I/O Interface



R4952 EPROM Gang Programmer

The R4952 gang programmer allows high-speed programming for up to ten 16-Mbit PROMs with large capacity and diverse packages. Simultaneous writing for up to 10 PROMS is possible.

The standard RS-232C and parallel interfaces allow files managed by the host computer to be transferred and remote-controlled. With the standard 8-Mbit buffer memory, the ADVANTEST's unique hard stack system makes it possible to reduce the overhead time and function execution time.

■ Can be Used for 16-Mbit PROMs

Typical EPROMs and EEPROMs with a capacity of 64 Kbits to 16 Mbits can be programmed with easy operation. (16-Mbit PROM programming is available only in the Master mode.)

High-Speed Programming Algorithm

Accommodates various high-speed programming algorithms including 4-byte and 2-word programming.

Reduction in Function Execution Time

By making ADVANTEST's unique hard stack system faster, the read verification time can be reduced to approx. 1/2 times. (Measured by ADVANTEST.)

■ Standard Serial I/O and Parallel I/O Interface

The standard RS-232C interface allows remote control and data input and output for the R4952 from the host computer. With the standard Centronics parallel interface, high-speed data input is possible.

For example, write data of the R4952 is transferrable to the host computer and the data can be compared and checked.

Compatible with Remote Control Software

Applicable devices:	
EPROM/EEPROM (including flash EEPROM) v	vith a capacity of 64
khits to 16 Mbits (16 Mbits: in the master mode	a)
* Applicable types depend on the socket adapt	tor.
* For details, see the device list for each manu	facturer.
Standard socket adaptor:	lation
R49512B, 32 pins. DIP type	
Simultaneous write operation: 1 to 10 ROMs	
Buffer memory capacity: 8 Mbits (1 Mbyte)	
Device functions:	
Blank check, Program, Erase, Read check, Cor	ov. B.P.R. sequence
E.B.P.R. sequence (with EEPROM), P.R. contin	uous operation
Data mode : Master mode. Buffer RAM mode	
Address modes:	
8-bit wide ROM: Normal. 16-bit split. 32-bit sp	lit
16-bit wide ROM: Normal (data exchangeable).	. 32-bit split (data
exchangeable), page splitting	,
Write modes:	
High-speed programming: Intel. Intel quick. Fi	iiitsu. AMD. T.I.
snap. and other mega-bit high-speed EPRON	I program of each
manufacturer.	- F8
Intelligent identifier mode (ID mode):	
ID AUTO: Automatically sets the ROM type.	
ID CHECK: Checks the set ROM type.	
ROM type selection : Scroll setting by means of th	he Maker and Type
kevs.	51
Output voltage comparison level:	
$V_{rr} + 0.5 V + 35 mV (I_{rr} = 1.8 mA)$	
$V_{0L} + 2.3 V \pm 35 mV$	
EPROM protective functions:	
Power-down at device insertion	
Check for reverse or improper insertion (ON	/OFF possible)
Data edit functions:	·····
RAM edit Checksum Data clear Clear move	
Reliability check functions:	
• V margin check (4 75 V 5 25 V)	
• V and V level check	
• Checksum	
Automatic setting functions	
(Backed up by the EEPROM)	
Type I/O conditions Translation format etc	
Set data hackun:	
Set data including ROM type haud rate transl	ation data hit
configuration etc. can be stored and recalled fr	om EEPROM
I/O Specifications	
Standard interface	
Serial I/O interface in conformance with RS	-232C
Baud rate: 110 to 19 200 bps	2080
Parity: Non even odd	
$\mathbf{X} \cap \mathbf{N} \mathbf{X} \cap \mathbf{F} \mathbf{F}$	
 Parallal I/O interface in conformance with O 	entronics
Constal Specifications	,cint Offics
Disnlaw Function state: Indicated by the LED inc	licator
Type softings: Displayed on the LCD screen in	16 characters v
2 lines	TO CHALACTERS X

Power supply: 90 to 132 VAC or 198 to 250 VAC (selectable by the rear switch)

Option No.	Standard	40
Power voltage (V)	90 to 132	198 to 250

Power frequency: 48 to 66 Hz

Power consumption: 70 VA or less

External dimensions: Approx. 364(W) × 80(H) × 256(D) mm For applicable socket adapters, see the description of socket adapters for programmers.