

CPCI-6115

CompactPCI Peripheral Slot Compute Blade

■ Embedded Computing for
Business-Critical Continuity™

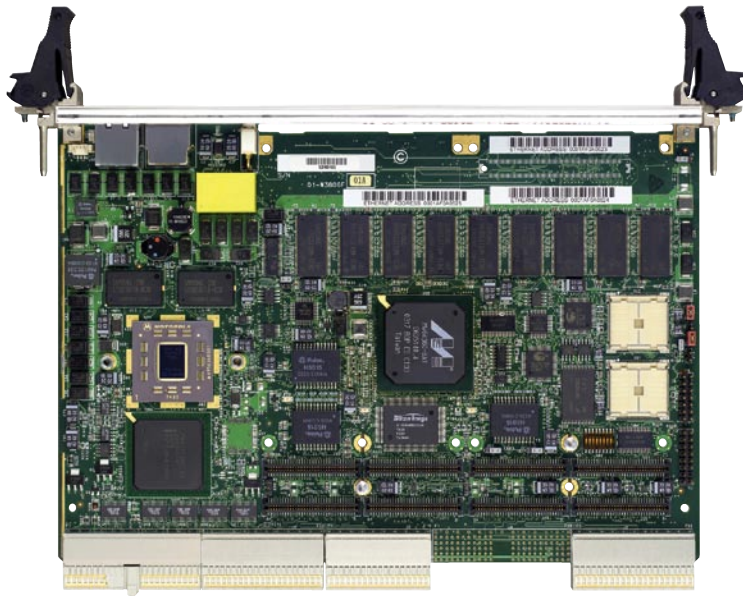
The high performance Emerson CPCI-6115 CompactPCI peripheral slot compute blade provides an excellent platform for compute-intensive applications such as call processing engines, gateway controllers and administration complexes interfacing to the central office.

- Up to 1.0 GHz MPC7457 PowerPC processor with AltiVec™ technology for peak performance
- Up to 2GB DDR ECC memory for peak computation power
- Dual Gigabit Ethernet PICMG 2.16 interface connectivity
- Additional Gigabit Ethernet interfaces for increased connectivity
- Two PMC sites for maximum I/O expansion
- Front panel or backplane I/O for popular chassis support

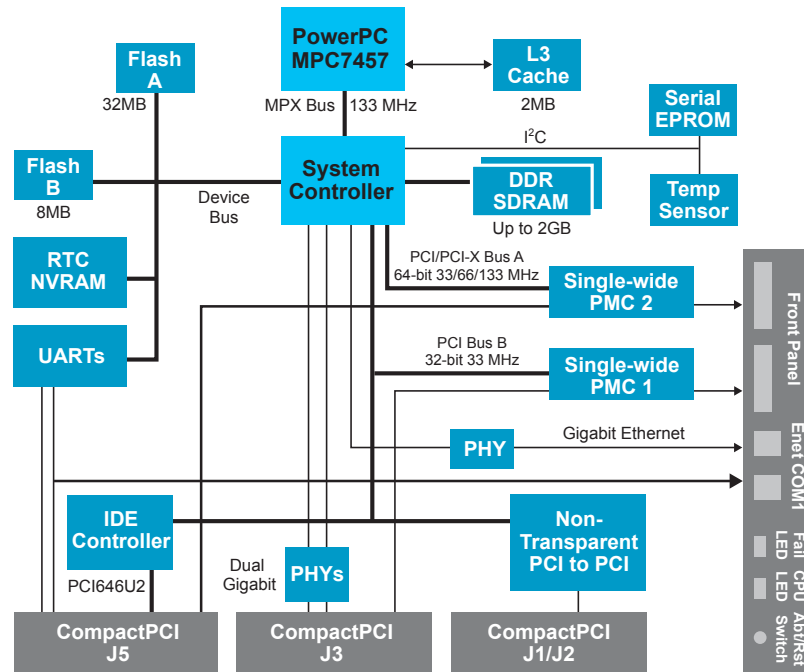
The high performance Emerson CPCI-6115 CompactPCI® peripheral slot compute blade provides an excellent platform for compute-intensive applications such as call processing engines, gateway controllers and administration complexes interfacing to the central office. All these applications are looking to handle greater workloads in the search for more cost effective solutions – the CPCI-6115 is being used to deploy such solutions.

At the core of the CPCI-6115 is a 1 GHz MPC7457 PowerPC® processor with AltiVec technology, 2MB of L3 cache and up to 2GB of DDR memory with ECC protection. Dual PMC sites allow for additional I/O as well as processor PMC offload engines to boost call processing capabilities even further.

PMC sites offer the additional advantage of PCI-X performance at speeds up to 133 MHz. The CPCI-6115 is also well suited for Ethernet connectivity applications requiring three Ethernet interfaces at Gigabit speeds and PICMG® 2.16 support.



CPCI6115 Block Diagram



Hardware

PROCESSOR

- MPC7457 at up to 1 GHz
- 2MB of L3 cache

ADDITIONAL I/O

- First PMC site on dedicated 133 MHz PCI-X bus
- Second PMC site on 33 MHz PCI bus
- Optional EIDE controller supports CompactFlash or drive solutions via RTM
- Dual async serial interfaces
- Real-time clock
- Watchdog timers

MEMORY

- Up to 2GB double data rate (DDR)
- ECC protected
- Memory resident on board to allow dual PMC sites
- Flash: Bank A is 32MB; Bank B is 8MB

NETWORK

- PICMG 2.16 via dual redundant Gigabit Ethernet controllers
- An additional Gigabit Ethernet interface routed to the front

Software

LINUX

Standard distributions of Linux (from 2.4 kernel) are supported

VXWORKS

A board support package for VxWorks is available

Standards Compatibility

Compatible with PICMG 2.0 (CompactPCI), PICMG 2.1 (Hot Swap), PICMG 2.16 specifications

Ordering Information	
Part Number	Description
CPCI-6115-220	867 MHz MPC7457 processor, 512MB ECC memory, two PMC sites, 5E
CPCI-6115-240	1.0GHZ MPC7457 processor, 512MB ECC memory, two PMC sites, 5E
CPCI-6115-270	1.0GHZ MPC7457 processor, 2.0GB ECC memory, two PMC sites, 5E
Transition Module	
CPCI-6115-MCPTM-02	Integrated (& FRU) transition module for the CPCI-6115, 5E
Documentation	
6806800A68A	CPCI-6115 CompactPCI Single Board Computer Installation and Use
MCPN905A/PG	MCPN905 CompactPCI Single board Computer Programmer's Reference Guide
MOTLODA/UM	MOTLoad Firmware Package User's Manual

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