

IP520 Octal Serial 232 Communication

These modules provide eight asynchronous serial communication ports from a single IP carrier slot. Software-configuration helps you quickly set baud rates, character-sizes, stop bits, and parity. Signal support for RTS/CTS handshaking is also included.

For more efficient data processing, each serial port is equipped with 64-character FIFO buffers on the transmit and receive lines.

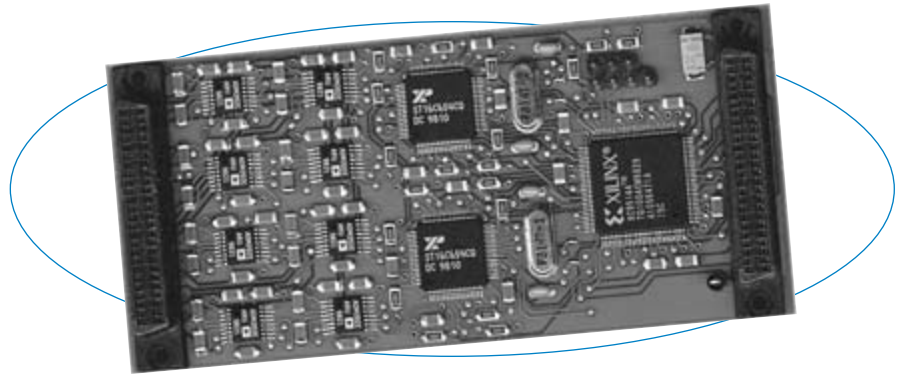
The data ports generate individually controlled transmit, receive, line status, and data set interrupts. Since unique interrupt vectors may be assigned to each port, it is easy for you to identify and locate the interrupt source. Also, a priority shifting scheme prevents continuous interrupts from one port from blocking interrupts from another.

Features

- Eight RS232E ports
- 64-byte transmit FIFO buffers
64-byte receive FIFO buffers
- Interrupts with unique vectors for each port
- Programmable baud rate (up to 230Kbps)
- Individual handshake lines (RTS, CTS) on each channel
- Line-break and false start-bit detection
- Industry-standard 16C654 family UART includes software-compatible 16C450 mode

Benefits

- High-density design lowers per-port costs and saves IP carrier card slots for other functions.
- 64-byte FIFO buffers minimize CPU interaction for improved system performance.
- Each serial channel provides handshake support to simplify interfacing with modems.



With eight serial ports per module, the IP520 provides a high-density solution to reduce costs and use fewer card slots.

Specifications

RS232E Serial Ports

Configuration: Independent, non-isolated serial ports with a common single return connection and configured as a DTE device.

Data rate: Programmable up to 230K bits/second using internal baud rate generator.

Max. cable length: 15 meters (50 feet) typical, limited to a cable capacitive load of 2500pF.

Character size: 5 to 8 bits, software-programmable.

Parity: Odd, even, or no parity; software-programmable.

Stop bits: 1, 1-1/2, or 2 bits; software-programmable.

Data register buffers: Double buffered or 64-byte FIFO buffered, mode selectable.

Interrupts: Receiver line status (overrun, parity, framing error, or break interrupt); received data available (FIFO level reached) or character time-out; transmitter (FIFO level reached); or modem status (CTS).

Environmental

Operating temperature: 0 to 70°C (IP520-64) or -40 to 85°C (IP520-64E).

Storage temperature: -55 to 125°C.

Relative humidity: 5 to 95% non-condensing.

Power: +5V (±5%): 340mA maximum.

MTBF: Consult factory.

IP Compliance (ANSI/VITA-4)

Meets IP specifications per ANSI/VITA-4 1996.

IP data transfer cycle types supported:

Input/output (IOSel*), ID read (IDSel*), Interrupt select (INTSel*).

Access times (8MHz clock):

ID PROM read: 0 wait state (250nS cycle).

Channel register read/write: 1 wait state (375nS cycle).

Interrupt register read/write: 2 wait states (500nS cycle).

Ordering Information

Industry Pack Modules IP520-64

Eight RS232E serial ports.

IP520-64E

Same as IP520-64 plus extended temperature range.

For Industry Pack Carrier Cards, see Page 5.

Customized Industry Pack Modules

† 5018-x

Modified IP520-64 with user specified crystal/baud rate.

† Specify x = crystal frequency when ordering.

Minimum quantity per order is two units.

For Industry Pack Carrier Cards, see Page 5.

Software (see Page 81)

IPSW-API-VXW

VxWorks® software support package

IPSW-API-QNX

QNX® software support package

IPSW-ATX-PCI

ActiveX®/OLE Controls 2.0 software package

For accessories information, see Page 87.